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Application Number	09/891,192
Filing Date	June 26, 2001
First Named Inventor	Shigeo IRIE
Group Art Unit	1756
Examiner Name	S. R. Mohamedulla
Attorney Docket Number	740819-586

Total Number of Pages in This Submission

ENCLOSURES (check all that apply)

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Remarks

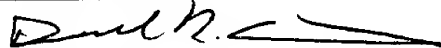
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Date

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#5/Election 3/3/03

Docket No. 740819-586

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shigeo IRIE

Serial No. 09/891,192

Filed: June 26, 2001

For: PATTERN FORMING METHOD AND
APPARATUS FOR FABRICATING
SEMICONDUCTOR DEVICE

)

) Group Art Unit: 1756

) Examiner: S. R. Mohamedulla

)

) Date: March 31, 2003

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

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GROUP 1700

The Examiner's Action dated March 7, 2003, has been received and its contents carefully noted. In view thereof, Applicant hereby elects, without traverse, Group I, Claims 1-12, drawn to a pattern forming method, for prosecution in the instant application. Accordingly, consideration and allowance of the instant application are now respectfully requested.

Respectfully submitted,

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